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Investigation of the avalanche delay effect in sine-gated single-photon detector

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Abstract. A sine-gated single-photon detector (SPD) intended for use in a quantum key distribution (QKD) system is considered in this paper. An "avalanche delay" effect in the sine-gated SPD is revealed. This effect consists in the appearance of an avalanche triggered at the next gate after the photon arrival gate. It has been determined experimentally that the nature of this effect is not related to the known effects of afterpulsing or charge persistence. This effect negatively affects the overall error rate in the QKD system. The influence of the main detector control parameters, such as temperature, gate amplitude and comparator's threshold voltage, on the avalanche delay effect was experimentally established.

Keywords: avalanche delay, single-photon avalanche diodes, single-photon detector

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Исследование эффекта задержки лавины в детекторе одиночных фотонов с синусоидальным стробированием

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Аннотация. В данной статье рассматривается детектор одиночных фотонов (ДФ) с синусоидальным стробированием, предназначенный для использования в системе квантового распределения ключей (КРК). Обнаружен эффект «задержки лавины» в результате эксплуатации такого детектора. Этот эффект негативно влияет на общий уровень ошибок в системе КРК. Экспериментально установлено влияние основных параметров управления детектором на эффект задержки лавины.

Ключевые слова: задержка лавины, однофотонные лавинные диоды, детектор одиночных фотонов

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Introduction

A single-photon detector (SPD) is a device capable of sensing single photons at a specific wavelength. Such a device has many applications [1–6], but the most promising application is in quantum key distribution (QKD) [7]. There are several types of devices that can be used as single-photon detectors [8]. The optimum device to create a miniaturised SPD and a compact QKD system as a whole is the InGaAs/InP based single-photon avalanche diode (SPAD).

It is important to minimise the level of SPD false triggers, which entails an increased error rate for a QKD system with such a detector as part of it. One way to keep noise to a minimum is to set the control parameters of the detector correctly.

One recently discovered effect is the avalanche delay effect, which causes false triggering of a sine-gated SPD in an adjacent gate. In this paper the influence of the detector control parameters on this negative effect is established experimentally.

Materials and Methods

A special setup was used to measure the SPD parameters. It includes a synchronization system, a laser radiation source, a system of beam splitters, a system of variable optical attenuators with controlled output power, an SPD under examination, and an oscilloscope. All components of the system are controlled by software created in the LabVIEW environment.

Results and Discussion

In our experiments we observe the effect of occurrence the avalanche triggers at the next gate after the photon arrival gate. We analyze the next possible reasons of occurrence of this effect. Our custom InGaAs/InP SPAD based SPD was tested to make a decision on what's the main reason of this shifted avalanche triggers. Let us determine experimentally whether the effects of the afterpulsing and the charge persistence effect affect the experimentally observed effect:

1) Afterpulsing effect: the initial (here we mean that this is the charge from first photon or from the low-amplitude and unregistered avalanche) charge trapped at the first gate, and release at the second gate. The approach to verify or falsify is to perform measurements with different temperatures. If histograms didn't change its form, it means that this effect is not depend on the temperature and is not due to simple trapping.

2) Charge persistence effect: The initial charge is trapped at the potential well at the absorption/grading regions heterointerface. This effect should be strongly depended on the gate amplitude and its bias and virtually not depend on the temperature.

It was found experimentally that the nature of this effect is not related to the known effects of afterpulsing or charge persistence. As we can see on the Fig. 1, there is not temperature dependence. All three figures have very similar histogram forms and relative peaks height and positions. So, that this effect is not due to afterpulsing effect. In Fig. 2 we can see that effect of triggering photon at the next gate has more manifestation at the low gate voltages. On the contrary, charge persistence effect has more manifestations at the high gate amplitude, as we conclude in our work [9]. This means that the effect is of a fundamentally new nature.

The initial avalanche is too low, and didn't have time to grow enough to trigger the comparator. In this case, there is a lot of free carriers at the structure, that will dissolve, and will not trigger the avalanche due to SPAD stays at the off state. But these free charges (or detrapped charges) will trigger the avalanche at the next gate. We can lower the comparator discrimination threshold V_{th} to increase the probability of comparator triggering by low amplitude avalanches. In this case we will see the lowering the second peak height.

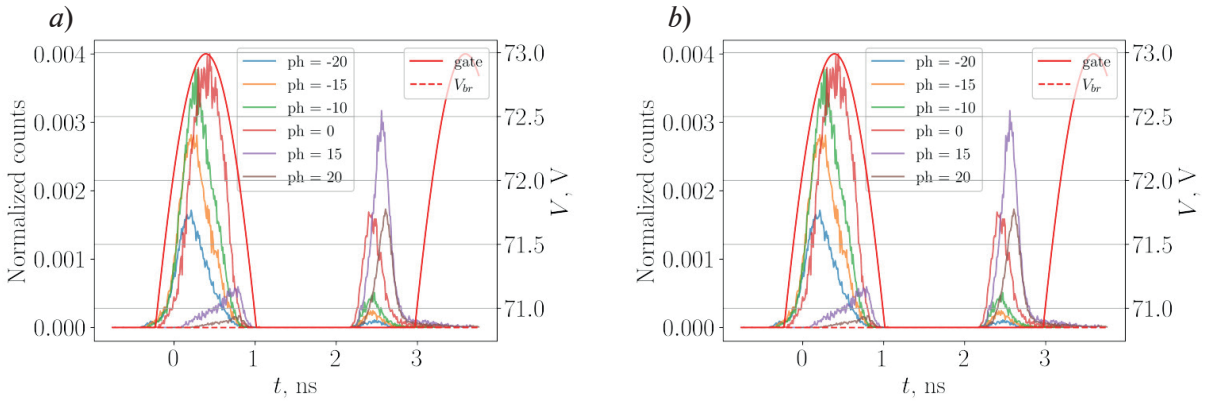


Fig. 1. Measured time resolution for SPD for different temperatures: $T = -35\text{ }^{\circ}\text{C}$ (a) and $T = -55\text{ }^{\circ}\text{C}$ (b). Gate amplitude is 3.25 V. The meaning of the phase $ph = 0$ denotes the maximum quantum efficiency. Shifted in according to ph . V_{br} – breakdown voltage

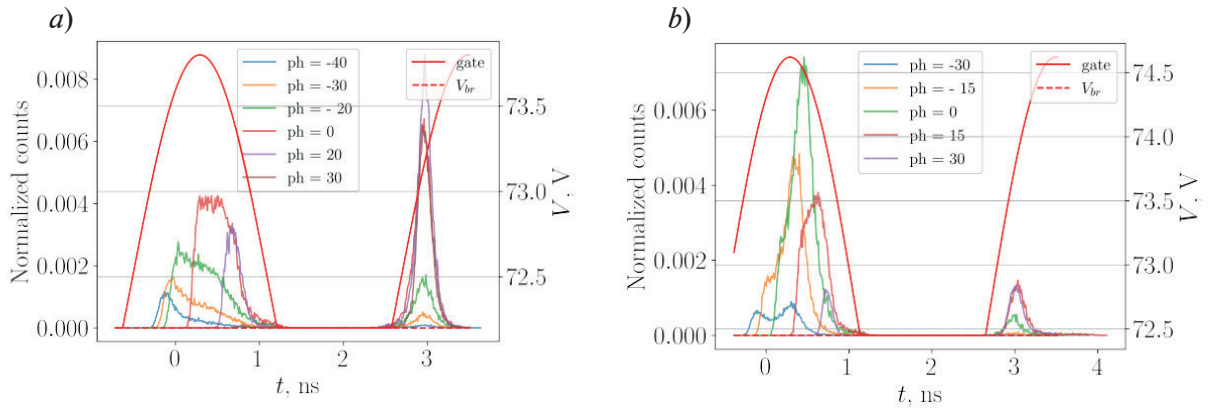


Fig. 2. Measured time resolution for SPD for different gate amplitudes: $V_g = 1.3\text{ V}$ (a); $V_g = 2\text{ V}$ (b). The meaning of the phase $ph = 0$ denotes the maximum quantum efficiency. Shifted in according to ph . V_{br} –breakdown voltage

Now we perform measurements with different comparator's threshold levels. We make measurements on the SPD with $V_g = 3.25\text{ V}$ and temperature $T = -50\text{ }^{\circ}\text{C}$. We tested the comparator's threshold voltage: $V_{th} = 1\text{ V}$ and $V_{th} = 1.35\text{ V}$. Then more this voltage is, than more probability, that small avalanche will trigger the comparator and consequently accounted. We present these measurements results on the Fig. 3.

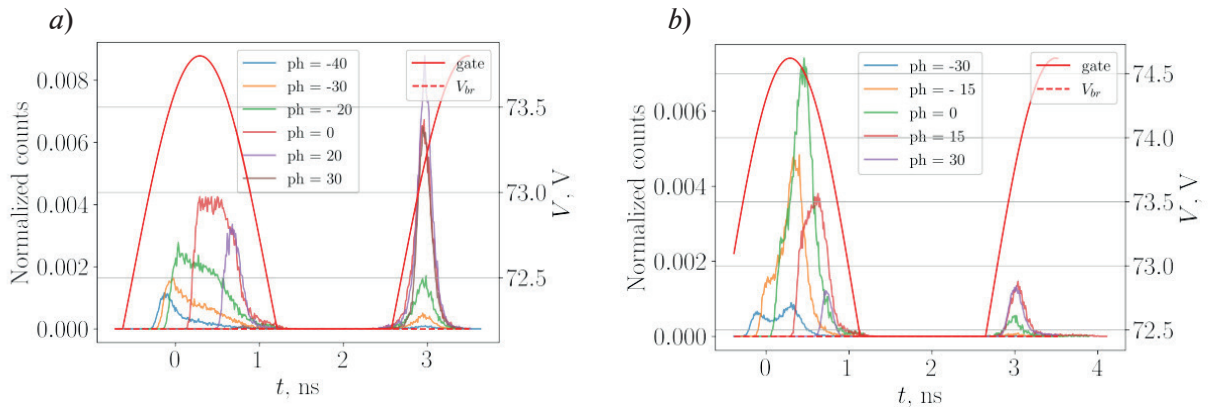


Fig. 3. Measured time resolution for SPD for different comparator's threshold voltage: $V_{th} = 1\text{ V}$ (a); $V_{th} = 1.35\text{ V}$ (b). The meaning of the phase $ph = 0$ denotes the maximum quantum efficiency. Shifted in according to ph . V_{br} –breakdown voltage

These figures show a strong dependence of the height of the second peak on the comparator threshold voltage: the height of the second peak decreases as this voltage increases.

Conclusion

We can conclude, that this effect is due to avalanche delay effect. The sense of this effect is the next: the avalanche at the end of the gate has no time to growing enough to be registered, and continue growing at the next gate, if has not been quenched by high gate voltage.

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